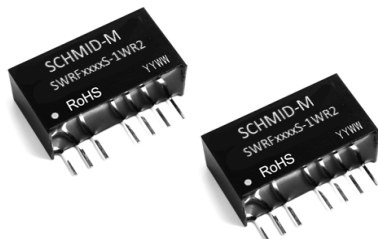




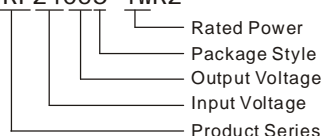
## SWRE\_S - 1WR2 & SWRF\_S-1WR2 Series

### 1W, WIDE INPUT, ISOLATED & REGULATED DUAL/SINGLE OUTPUT DC-DC CONVERTER



#### PART NUMBER SYSTEM

SWRF2405S-1WR2



#### FEATURES

- Ultra-Miniature SIP Package
- 2:1 wide input voltage range
- Operating temperature range: -40°C ~ +85°C
- 3KVDC isolation
- Short circuit protection(automatic recovery)
- External On/Off control
- High Power Density

#### APPLICATION

The SWRE\_S-1WR2 & SWRF\_S-1WR2 Series are specially designed for applications where a wide range input voltage power supplies are isolated from the input power supply in a distributed power supply system on a circuit board. For these DC-DC converters, You can reduce the design point of failure and save the development of micro power supply's manpower, material and time costs, also better ensure product quality stability, protect safety and reliability of the end of products.

These products apply to where:

- 1) Input voltage range  $\leq 2:1$ ;
- 2) Input and output isolation  $\leq 3KVDC$ ;
- 3) Regulated and low ripple noise is required.

Such as: industrial control, telecommunication etc.

#### SELECTION GUIDE

Model	Input Voltage(VDC)		Output Voltage (VDC)	Output Current (mA)		Input Current (mA)(typ.)		Reflected Ripple Current (mA,typ.)	Max. Capacitive Load <sup>②</sup> (μF)	Efficiency (% ,typ.) @Max. Load
	Nominal (Range)	Max. ①		Max.	Min.	@Max. Load	@No Load			
SWRE0505S-1WR2	5 (4.5-9)	11	±5	±100	±5	274	25	30	1000	73
SWRE0512S-1WR2			±12	±42	±2	263			470	76
SWRE0515S-1WR2			±15	±33	±2	267			330	75
SWRF0505S-1WR2			5	200	10	278			2200	72
SWRF0512S-1WR2			12	83	4	263			1000	76
SWRF0515S-1WR2			15	67	3	267			680	75
SWRE1205S-1WR2			12 (9-18)	20	±5	±100			±5	107
SWRE1212S-1WR2	±12	±42			±2	103	470	81		
SWRE1215S-1WR2	±15	±33			±2	104	330	80		
SWRF1203S-1WR2	3.3	303			15	112	2700	75		
SWRF1205S-1WR2	5	200			10	108	2200	77		
SWRF1209S-1WR2	9	111			6	106	1800	79		
SWRF1212S-1WR2	12	83			4	104	1000	80		
SWRF1215S-1WR2	15	67			3	104	680	80		
SWRE2405S-1WR2	24 (18-36)	40			±5	±100	±5	52	6	55
SWRE2412S-1WR2			±12	±42	±2	52	470	80		
SWRE2415S-1WR2			±15	±33	±2	52	330	80		
SWRF2403S-1WR2			3.3	303	15	56	2700	75		
SWRF2405S-1WR2			5	200	10	54	2200	77		
SWRF2412S-1WR2			12	83	4	51	1000	81		
SWRF2415S-1WR2			15	67	3	53	680	79		
SWRF2424S-1WR2			24	42	2	54	470	77		
SWRE4805S-1WR2			48 (36-75)	80	±5	±100	±5	27		
SWRE4812S-1WR2	±12	±42			±2	26	470	80		
SWRE4815S-1WR2	±15	±33			±2	26	330	80		
SWRF4803S-1WR2	3.3	303			15	28	2700	75		
SWRF4805S-1WR2	5	200			10	27	2200	76		

SWRF4812S-1WR2	48 (36-75)	80	12	83	4	26	4	70	1000	81
SWRF4815S-1WR2			15	67	3	26			680	80

Note: ①. Absolute maximum rating without damage on the converter, but it isn't recommended;  
②. For dual output converter, the given value is the same for each output.

INPUT SPECIFICATIONS					
Item	Test Conditions	Min.	Typ.	Max.	Unit
Input Surge Voltage (1sec. max.)	5V input	-0.7	--	12	VDC
	12V input	-0.7	--	25	
	24V input	-0.7	--	50	
	48V input	-0.7	--	100	
Start-up Voltage	5V input	3.5	4	4.5	
	12V input	4.5	8	9	
	24V input	11	16	18	
	48V input	24	33	36	
Input Filter		C Filter			
Ctrl*	Models ON	Ctrl open or be insulated			
	Models OFF	Connect high level voltage, and ensure the current into Ctrl to be 5-10mA			

Note: \*Please refer to "DESIGN CONSIDERATIONS" as the direction for use of Ctrl .

OUTPUT SPECIFICATIONS					
Item	Test Conditions	Min.	Typ.	Max.	Unit
Output Voltage Accuracy	5% to 100% load	--	±1	±3	%
No-load Output Voltage Accuracy	Input voltage range	--	±1.5	±5	
Output Voltage Balance	Dual output, balanced loads	--	±0.3	±0.5	
Line Regulation	Full load, Input voltage from low to high	--	±0.2	±0.5	
Load Regulation	5% to 100% load	--	±0.4	±0.75	
Transient Recovery Time	25% load step change	--	0.5	2	ms
Transient Response Deviation		--	±2.5	±5	%
Temperature coefficient	100% load	--	±0.02	±0.03	%/°C
Ripple *	20MHz Bandwidth	--	30	50	mVp-p
Noise*		--	50	100	
Output Short Circuit Protection		Continuous, automatic recovery			

Note: \*Ripple and noise tested with "parallel cable" method. See detailed operation instructions at *DC-DC application notes*.

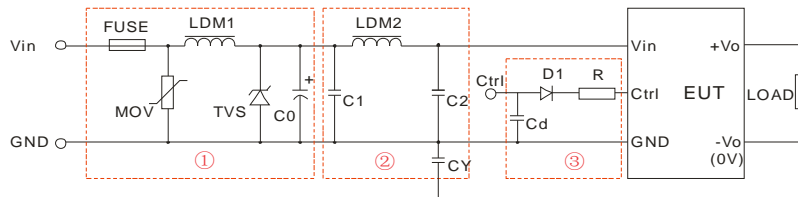
COMMON SPECIFICATIONS					
Item	Test Conditions	Min.	Typ.	Max.	Unit
Isolation Voltage	Input-Output, Tested for 1 minute, leakage current less than 1 mA	3000	--	--	VDC
Isolation Resistance	Input-Output, Test at 500VDC	1000	--	--	MΩ
Isolation Capacitance	Input-Output, 100KHz/0.1V	--	30	50	pF
Switching Frequency(PFM Mode)	100% load, Nominal Input voltage	--	200	--	KHz
MTBF	MIL-HDBK-217F @25°C	1000	--	--	K hours
Case Material		Plastic ( UL94-V0 )			
Weight		--	4.9	--	g

ENVIRONMENTAL SPECIFICATIONS					
Item	Test Conditions	Min.	Typ.	Max.	Unit
Storage Humidity	Non condensing	--	--	95	%
Operating Temperature	Power derating (above85°C, see Figure 5)	-40	--	85	°C
Storage Temperature		-55	--	125	
Temp. rise at full load	Ta=25°C	--	25	--	
Lead Temperature	1.5mm from case for 10 seconds	--	--	300	
Cooling		Free air convection			

## EMC SPECIFICATIONS

EMI	CE	CISPR22/EN55022	CLASS B (Recommended Circuit Refer to Figure1-② or Figure 3)	
	RE	CISPR22/EN55022	CLASS B (Recommended Refer to Figure1-② or Figure 3)	
EMS	ESD	IEC/EN61000-4-2	Contact ±4KV	perf. Criteria B
	RS	IEC/EN61000-4-3	10V/m	perf. Criteria A
	EFT	IEC/EN61000-4-4	±2KV (Recommended Circuit Refer to Figure1-①)	perf. Criteria B
		IEC/EN61000-4-4	±4KV (Recommended Circuit Refer to Figure 3)	perf. Criteria B
	Surge	IEC/EN61000-4-5	±2KV (Recommended Circuit Refer to Figure1-① or Figure 3)	perf. Criteria B
	CS	IEC/EN61000-4-6	3 Vr.m.s	perf. Criteria A
Voltage dips, short and interruptions immunity		IEC/EN61000-4-29	0%-70%	perf. Criteria B

## EMC RECOMMENDED CIRCUIT



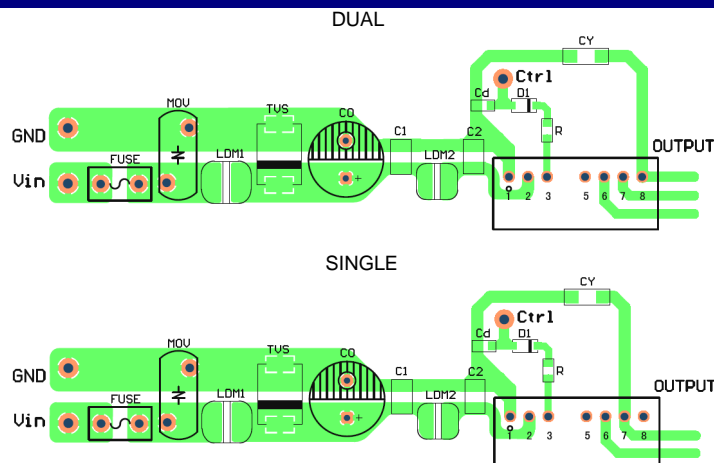
(Figure1)

Recommended external circuit parameters:

Model	Vin:5V	Vin:12V	Vin:24V	Vin:48V
FUSE	Choose according to practical input current			
MOV	--	--	S14K35	S14K60
LDM1	--	--	56μH	56μH
TVS	SMCJ13A	SMCJ28A	SMCJ48A	SMCJ90A
C0	680μF/16V	680μF/25V	330μF/50V	330μF/100V
C1	4.7μF/50V			4.7μF/100V
LDM2	12μH			
C2	4.7μF/50V			4.7μF/100V
CY	1nF/3KV			
D1	RB160M-60/1A			
R	Follows: $R = \frac{V_C - V_D - 1.0}{I_C} - 300$			
Cd	47nF/100V			

- Note:1. In Figure 1, part ① is EMS recommended external circuit, part ② is EMI recommended external circuit. Choose according to requirements;  
 2.  $V_C$  is the voltage to GND from Ctrl,  $V_D$  is the forward conduction voltage drop of D1,  $I_C$  is the current through Ctrl pin which is normally 5-10mA, the external circuit of Ctrl is as shown in figure1-③;  
 3. If there is no recommended parameters, the model no require the external component.

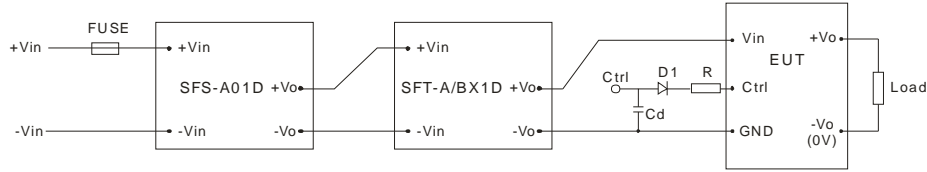
## EMC RECOMMENDED CIRCUIT PCB LAYOUT



(Figure 2)

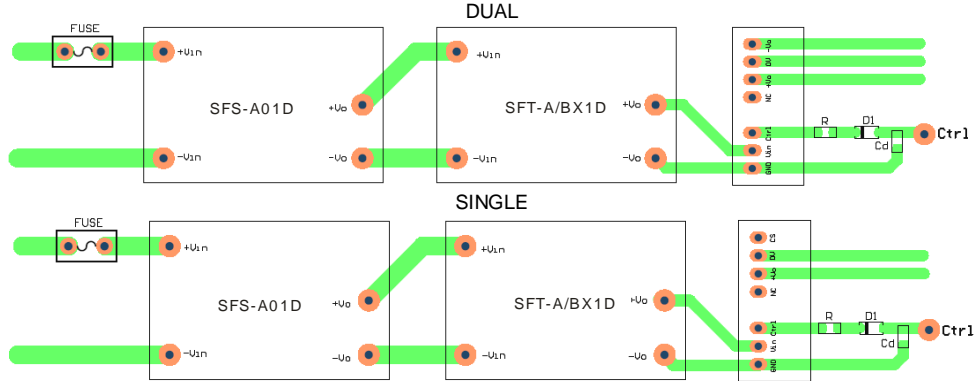
Note: The pad space between input and output GND (CY) must ≥2mm.

## EMC MODULE APPLICATION CIRCUIT



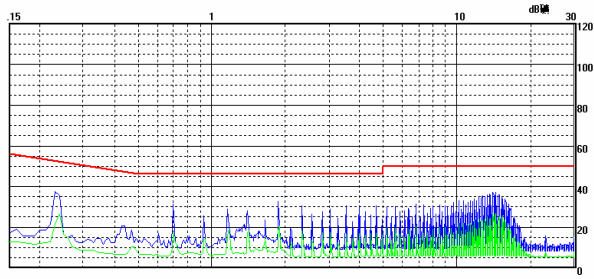
SFS-A01D, SFT-A/BX1D are SCHMID-M's EFT suppressors  
(Figure 3)

## EMC MODULE RECOMMENDED CIRCUIT PCB LAYOUT

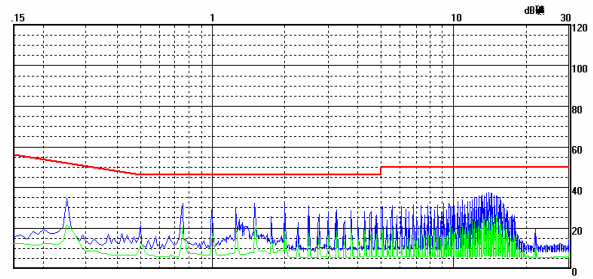


(Figure 4)

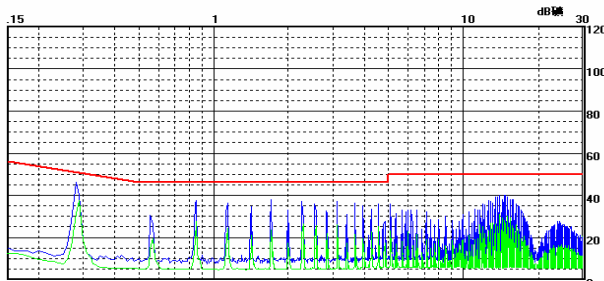
## EMI TEST WAVEFORM (RECOMMENDED CIRCUIT FIGURE 1-②)



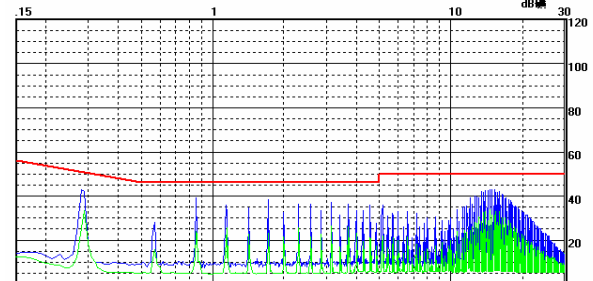
SWRE2405S-1WR2 CE(Class B, Positive line)



SWRE2405S-1WR2 CE(Class B, Negative line)



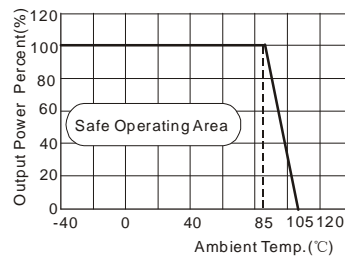
SWRF2405S-1WR2 CE(Class B, Positive line)



SWRF2405S-1WR2 CE(Class B, Negative line)

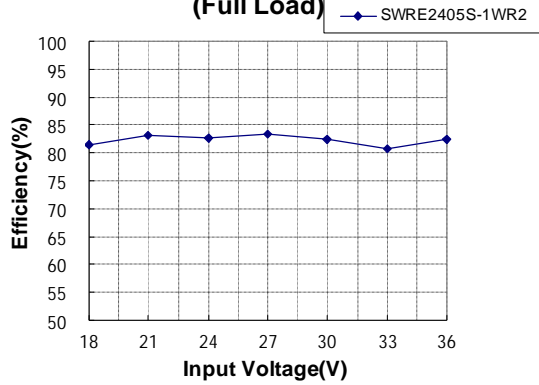
## PRODUCT TYPICAL PERFORMANCE CURVE

Temperature Derating Graph

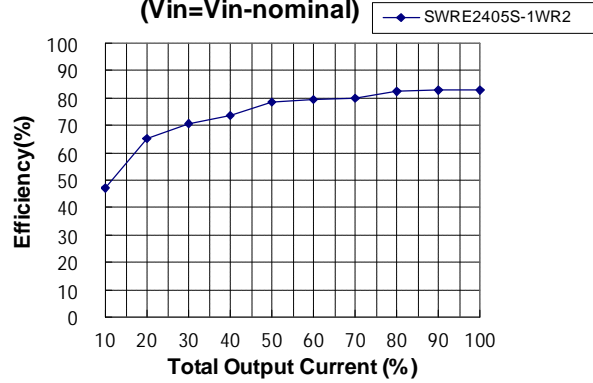


(Figure 5)

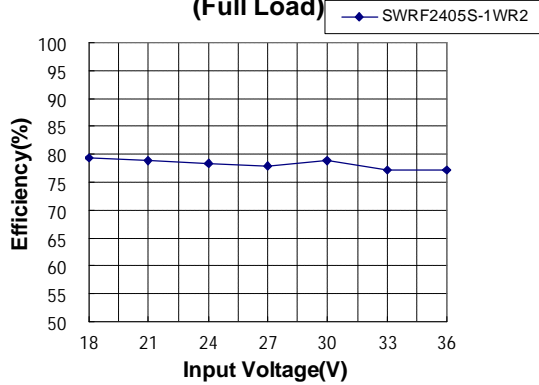
**Efficiency VS Input Voltage curve  
(Full Load)**



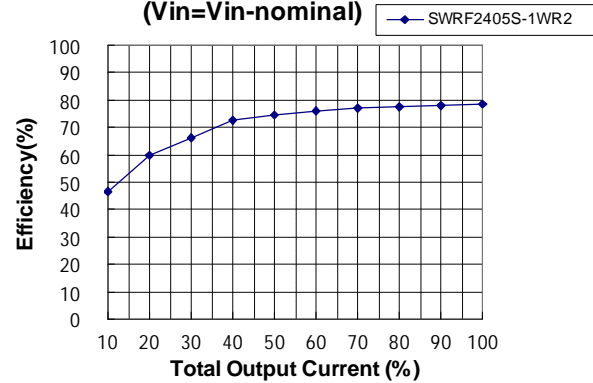
**Efficiency VS Output Load curve  
(Vin=Vin-nominal)**



**Efficiency VS Input Voltage curve  
(Full Load)**

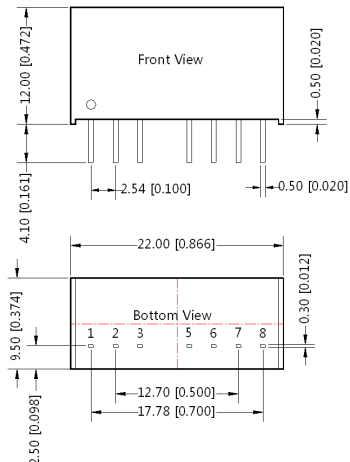


**Efficiency VS Output Load curve  
(Vin=Vin-nominal)**



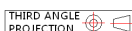
**OUTLINE DIMENSIONS, RECOMMENDED FOOTPRINT & PACKAGING**

**MECHANICAL DIMENSIONS**

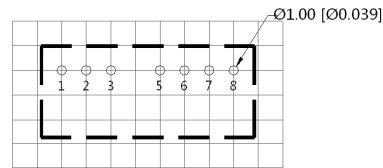


Pin	Single	Dual
1	GND	GND
2	Vin	Vin
3	Ctrl	Ctrl
5	NC	NC
6	+Vo	+Vo
7	0V	0V
8	CS	-Vo

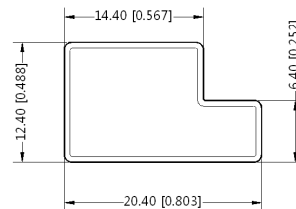
NC: No connection  
 Note :  
 Unit:mm[inch]  
 Pin section tolerances:±0.10[±0.004]  
 General tolerances:±0.25[±0.01]



**RECOMMENDED FOOTPRINT DETAILS**



**TUBE PACKAGING DIMENSIONS**

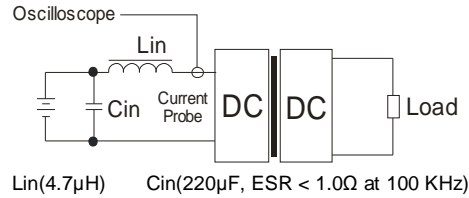


Note :  
 Unit:mm[inch]  
 General tolerances:±0.5[±0.02]  
 L=530[20.866] Tube Quantity:22 pcs  
 L=220[8.661] Tube Quantity:8 pcs  
 Inner carton(S): L\*W\*H=255\*170\*80  
 Outer carton(S): L\*W\*H=375\*280\*270, 6 inner cartons(S)  
 Inner carton(L): L\*W\*H=580\*200\*100  
 Outer carton(L): L\*W\*H=600\*215\*220, 2 inner cartons(L)  
 Outer carton(L): L\*W\*H=600\*215\*325, 3 inner cartons(L)

## TEST CONFIGURATIONS

### Input Reflected-Ripple Current Test Setup

Input reflected-ripple current is measured with an inductor  $L_{in}$  and Capacitor  $C_{in}$  to simulate the source impedance.



## DESIGN CONSIDERATIONS

### 1) Requirement on output load

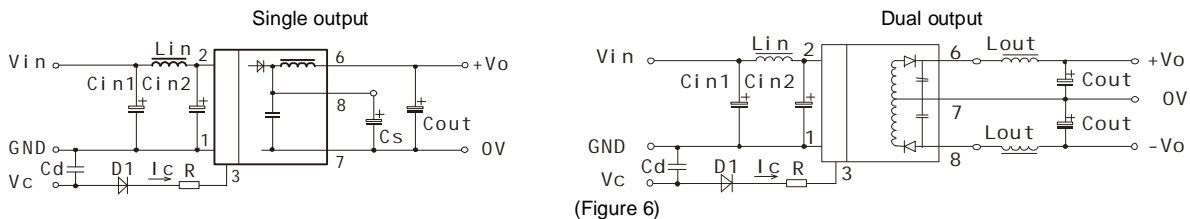
To ensure this module can operate efficiently and reliably, during operation, the minimum output load could not be less than 5% of the full load, otherwise output ripple may increase dramatically. If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load, suppose to use the resistance of 5% rated power, or use our company's products with a lower rated output power.

### 2) Recommended Circuit

All the SWRE\_S-1WR2 & SWRF\_S-1WR2 series have been tested according to the following recommended test circuit before leaving the factory (see Figure 6).

If you want to further decrease the input/output ripple, you can increase a capacitance-values properly or choose capacitors with low ESR. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance must be less than the Max. Capacitive Load.

General:  $C_{in1}$ : 5V&12V 100µF  
 24V&48V 10µF  
 $C_{in2}$ : 5V&12V 47µF  
 24V&48V 1µF  
 $L_{in}$ : 4.7µH~12µH  
 $C_s$ : 10µF~22µF  
 $C_{out}$ : 100µF(Typ.)  
 $L_{out}$ : 2.2µH~10µH  
 $C_d$ : 47nF/100V



(Figure 6)

### 3) Ctrl Terminal

When open or high impedance, the converter works well; When this pin is 'high', the converter shut down. It should be note that the input current should be between 5-10mA, exceeding the maximum 20mA will cause permanent damage to the converter. The value of R can be derived as follows:

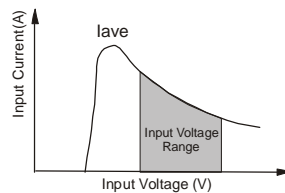
$$R = \frac{V_C - V_D - 1.0}{I_C} - 300$$

For Detailed parameter, please refer to "EMC RECOMMENDED CIRCUIT".

### 4) Input Current

When it is used in unregulated power supply, be sure that the fluctuating range of the power supply and the rippled voltage do not exceed the module standard. Input current of power supply should afford the flash startup average current of this kind of DC/DC module (Figure 7).

General:  $V_{in}=5V$   $I_{ave}=450mA$   
 $V_{in}=12V$   $I_{ave}=220mA$   
 $V_{in}=24V$   $I_{ave}=110mA$   
 $V_{in}=48V$   $I_{ave}=55mA$



(Figure 7)

5) It is not recommended to increase the output power capability by connecting two or more converters in parallel. The product is not hot-swappable

Note:

1. Min. load shouldn't be less than 5%, otherwise ripple maybe increased dramatically. If the product operates under min. load, it may not be guaranteed to meet all specifications listed. Operation under minimum load will not damage the converter.
2. Recommended Dual output models unbalanced load is  $\leq \pm 5\%$ , if the product operates  $> \pm 5\%$ , it may not be guaranteed to meet all specifications listed. Please contact our technical support for more details.
3. Max. Capacitive Load is tested at input voltage range and full load.
4. All specifications measured at  $T_a = 25^\circ\text{C}$ , humidity  $< 75\%$ , nominal input voltage and rated output load unless otherwise specified.
5. In this datasheet, all test methods are based on our corporate standards.
6. All characteristics are for listed models, and non-standard models may perform differently. Please contact our technical support for more details.
7. Please contact our technical support for any specific requirement.
8. Specifications of this product are subject to changes without prior notice.