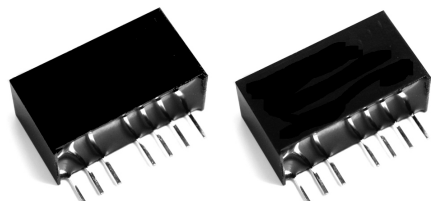


SCHMID-M

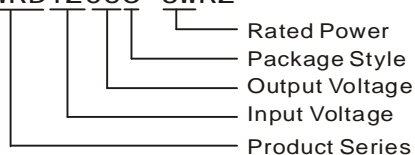
SWRA_S - 3WR2 & SWRB_S - 3WR2 Series

**3W, WIDE INPUT, ISOLATED & REGULATED
DUAL/SINGLE OUTPUT DC-DC CONVERTER**



PART NUMBER SYSTEM

SWRB1205S-3WR2



FEATURES

- Ultra-Miniature SIP Package
- 2:1 wide input voltage range
- Temperature range: -40°C ~ +85°C
- 1.5KVDC isolation
- Short Circuit Protection(automatic recovery)
- External On/Off control
- High Power Density

APPLICATION

The SWRA_S-3WR2 & SWRB_S-3WR2 Series are specially designed for applications where a wide range input voltage power supplies are isolated from the input power supply in a distributed power supply system on a circuit board. For these DC-DC converters, you can reduce the design point of failure and save the development of micro power supply's manpower, material and time costs, also better ensure product quality stability, protect safety and reliability of the end of products.

These products apply to where:

- 1) Input voltage range $\leq 2:1$;
- 2) 1.5KVDC input and output isolation;
- 3) Regulated and low ripple noise is required.

Such as: industrial control, tele-communications etc.

SELECTION GUIDE

Model	Input Voltage(VDC)		Output Voltage (VDC)	Output Current (mA)		Input Current (mA)(typ.)		Reflected Ripple Current (mA,typ.)	Max. Capacitive Load ^② (μF)	Efficiency (% , typ.) @ Max. Load
	Nominal (Range)	Max. ①		Max.	Min.	@Max. Load	@No Load			
SWRA0505S-3WR2	5 (4.5-9)	11	±5	±250	±13	676	40	30	1000	74
SWRA0512S-3WR2			±12	±104	±5	650			470	77
SWRA0515S-3WR2			±15	±83	±4	650			330	77
SWRB0505S-3WR2			5	500	25	685			2200	73
SWRB0509S-3WR2			9	278	14	676			1000	74
SWRB0512S-3WR2			12	208	10	650			680	77
SWRB0515S-3WR2			15	167	8	676			470	74
SWRA1205S-3WR2			12 (9-18)	20	±5	±300			±15	321
SWRA1212S-3WR2	±12	±125			±6	317	470	79		
SWRA1215S-3WR2	±15	±100			±5	313	330	80		
SWRB1203S-3WR2	3.3	758			38	278	2700	75		
SWRB1205S-3WR2	5	600			30	329	2200	76		
SWRB1209S-3WR2	9	333			17	317	1000	79		
SWRB1212S-3WR2	12	250			13	305	680	82		
SWRB1215S-3WR2	15	200			10	302	470	83		
SWRB1224S-3WR2	24	125			6	309	330	81		
SWRA2405S-3WR2	24 (18-36)	40			±5	±300	±15	158	7	110
SWRA2409S-3WR2			±9	±167	±8	155	680	81		
SWRA2412S-3WR2			±12	±125	±6	151	470	83		
SWRA2415S-3WR2			±15	±100	±5	151	330	83		
SWRB2403S-3WR2			3.3	758	38	141	2700	74		
SWRB2405S-3WR2			5	600	30	155	2200	81		
SWRB2409S-3WR2			9	333	17	151	1000	83		
SWRB2412S-3WR2			12	250	13	151	680	83		
SWRB2415S-3WR2			15	200	10	151	470	83		
SWRB2424S-3WR2			24	125	6	151	330	83		

SWRA4805S-3WR2	48 (36-75)	80	±5	±300	±15	79	7	45	1000	79
SWRA4812S-3WR2			±12	±125	±6	76			470	82
SWRA4815S-3WR2			±15	±100	±5	76			330	82
SWRB4803S-3WR2			3.3	758	38	70			2700	75
SWRB4805S-3WR2			5	600	30	82			2200	76
SWRB4812S-3WR2			12	250	13	78			680	80
SWRB4815S-3WR2			15	200	10	75			470	84
SWRB4824S-3WR2			24	125	6	76			330	82

Note: ①. Absolute maximum rating without damage on the converter, but it isn't recommended;
 ②. For dual output converter, the given value is the same for each output.

INPUT SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit
Input Surge Voltage (1sec. max.)	5V input	-0.7	--	12	VDC
	12V input	-0.7	--	25	
	24V input	-0.7	--	50	
	48V input	-0.7	--	100	
Start-up Voltage	5V input	3.5	4	4.5	
	12V input	4.5	8	9	
	24V input	11	16	18	
	48V input	24	33	36	
Input Filter		C Filter			
Ctrl*	Models ON	Ctrl open or be insulated			
	Models OFF	Connect high level voltage, and ensure the current into Ctrl to be 5-10mA			

Note: *Please refer to "DESIGN CONSIDERATIONS" as the direction for use of Ctrl.

OUTPUT SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit
Output Voltage Accuracy	5% to 100% load	--	±1	±3	%
No-load Output Voltage Accuracy ①	Input voltage range	--	±1.5	±5	
Output Voltage Balance	Dual output, balanced loads	--	±0.5	±1	
Line Regulation	Full load, Input voltage from low to high	--	±0.2	±0.5	
Load Regulation	5% to 100% load	--	±0.6	±1	
Transient Recovery Time	25% load step change	--	0.5	3	ms
Transient Response Deviation		--	±2.5	±5	%
Temperature coefficient	100% load	--	±0.02	±0.03	%/°C
Ripple ②	20MHz Bandwidth	--	30	45	mVp-p
Noise ②		--	35	75	
Output Short Circuit Protection		Continuous, automatic recovery			

Note: ①. The max. no-load output voltage accuracy for SWRB1203S-3WR2 and SWRB4803S-3WR2 is ±8%;

②. Ripple and noise tested with "parallel cable" method. See detailed operation instructions at *DC-DC application notes*;
 The max. output ripple for SWRA2405S-3WR2 is 65mVp-p.

COMMON SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit
Isolation Voltage	Tested for 1 minute, leakage current less than 1 mA	1500	--	--	VDC
Isolation Resistance	Test at 500VDC	1000	--	--	MΩ
Isolation Capacitance	Input/Output, 100KHz/0.1V	--	120	--	pF
Switching Frequency(PFM Mode)	100% load, Nominal Input voltage	--	250	--	KHz
MTBF	MIL-HDBK-217F@25°C	1000	--	--	K hours
Case Material		Plastic (UL94-V0)			
Weight		--	4.9	--	g

ENVIRONMENTAL SPECIFICATIONS

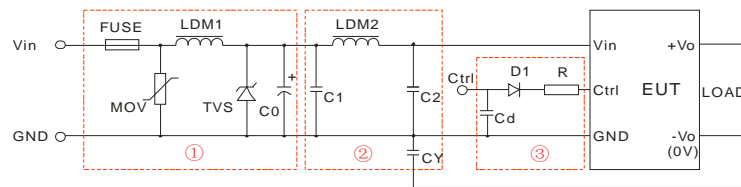
Item	Test Conditions	Min.	Typ.	Max.	Unit
Storage Humidity	Non condensing	--	--	95	%
Operating Temperature	Power derating (above85°C, see Figure 5)	-40	--	85	°C

Storage Temperature		-55	--	125
Temp. rise at full load	Ta=25°C	--	25	--
Lead Temperature	1.5mm from case for 10 seconds	--	--	300
Cooling		Free air convection		

EMC SPECIFICATIONS

EMI	CE	CISPR22/EN55022	CLASS B (Recommended Circuit Refer to Figure1-② or Figure 3)		
	RE	CISPR22/EN55022	CLASS B (Recommended Circuit Refer to Figure1-② or Figure 3)		
EMS	ESD	IEC/EN61000-4-2	Contact ±4KV	perf. Criteria B	
	RS	IEC/EN61000-4-3	10V/m	perf. Criteria A	
	EFT	IEC/EN61000-4-4	±2KV	(Recommended Circuit Refer to Figure1-①)	perf. Criteria B
		IEC/EN61000-4-4	±4KV	(Recommended Circuit Refer to Figure 3)	perf. Criteria B
	Surge	IEC/EN61000-4-5	±2KV	(Recommended Circuit Refer to Figure1-① or Figure 3)	perf. Criteria B
	CS	IEC/EN61000-4-6	3 Vr.m.s		perf. Criteria A
Voltage dips, short and interruptions immunity		IEC/EN61000-4-29	0%-70%	perf. Criteria B	

EMC RECOMMENDED CIRCUIT



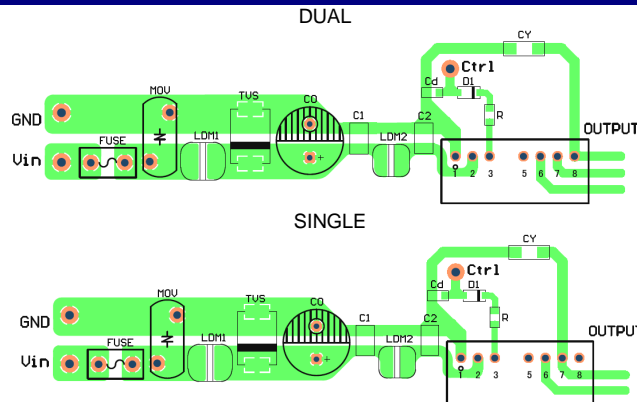
(Figure1)

Recommended external circuit parameters:

Model	Vin: 5V	Vin:12V	Vin:24V	Vin:48V
FUSE	Choose according to practical input current			
MOV	--	--	S14K35	S14K60
LDM1	--	--	56μH	56μH
TVS	SMCJ13A	SMCJ28A	SMCJ48A	SMCJ90A
C0	680μF/16V	680μF/25V	330μF/50V	330μF/100V
C1	4.7μF/50V			4.7μF/100V
LDM2	12μH			
C2	4.7μF/50V			4.7μF/100V
CY	1nF/2KV			
D1	RB160M-60/1A			
R	Follows: $R = \frac{V_c - V_D - 1.0}{I_c} - 300$			
Cd	47nF/100V			

- Note: 1. In Figure 1, part ① is EMS recommended external circuit, part ② is EMI recommended external circuit. Choose according to requirements;
 2. V_c is the voltage to GND from Ctr1, V_D is the forward conduction voltage drop of D1, I_c is the current through Ctr1 pin which is normally 5-10mA, the external circuit of Ctr1 is as shown in figure1-③;
 3. If there is no recommended parameters, the model no require the external component.

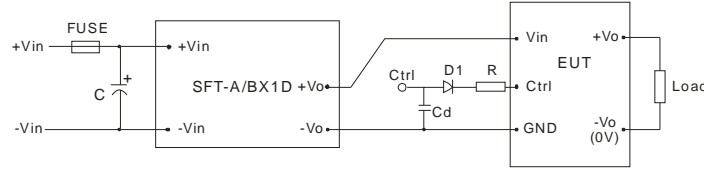
EMC RECOMMENDED CIRCUIT PCB LAYOUT



(Figure 2)

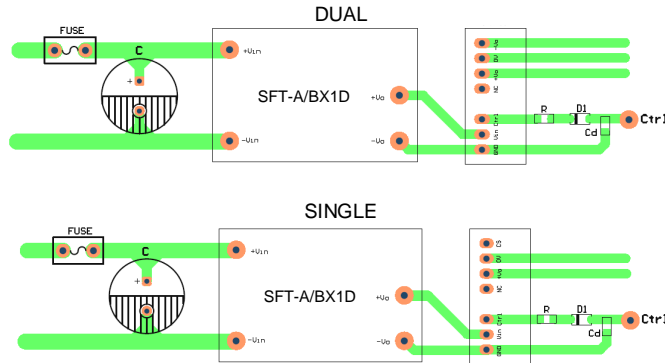
Note: The pad space between input and output GND (CY) must ≥2mm.

EMC MODULE APPLICATION CIRCUIT



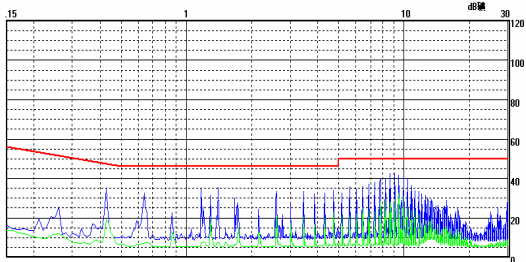
SFT-A/BX1D is SCHMID-M's EFT suppressor
 For Nominal Voltage <math><48V</math>,
 For Nominal Voltage =
 (Figure 3)

EMC MODULE RECOMMENDED CIRCUIT PCB LAYOUT

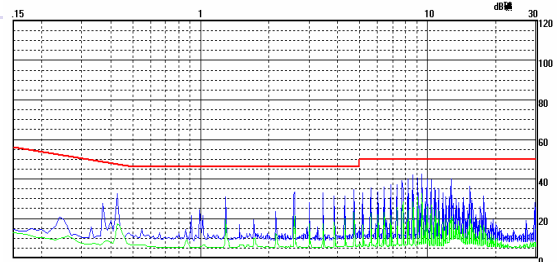


(Figure 4)

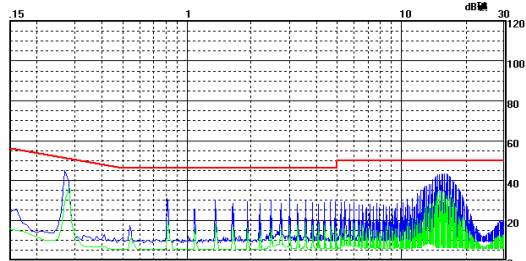
EMI TEST WAVEFORM (RECOMMENDED CIRCUIT FIGURE1-②)



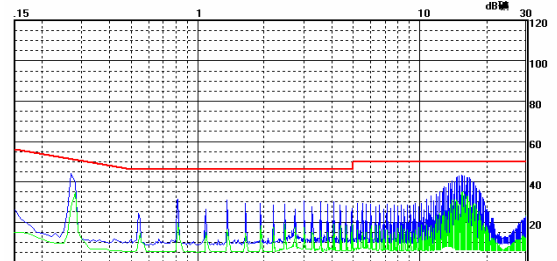
SWRA2415S-3WR2 CE(Class B, Positive line)



SWRA2415S-3WR2 CE(Class B, Negative line)

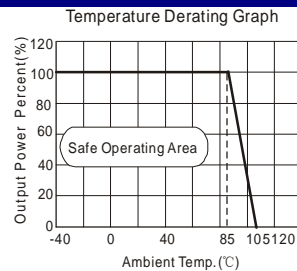


SWRB1205S-3WR2 CE(Class B, Positive line)



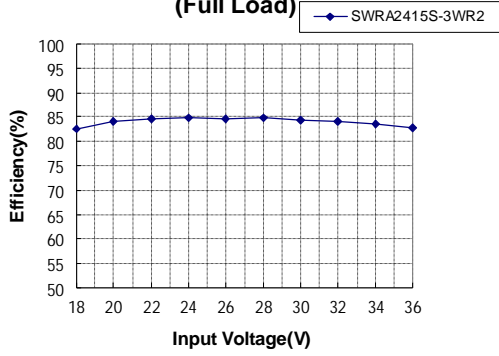
SWRB1205S-3WR2 CE(Class B, Negative line)

PRODUCT TYPICAL PERFORMANCE CURVE

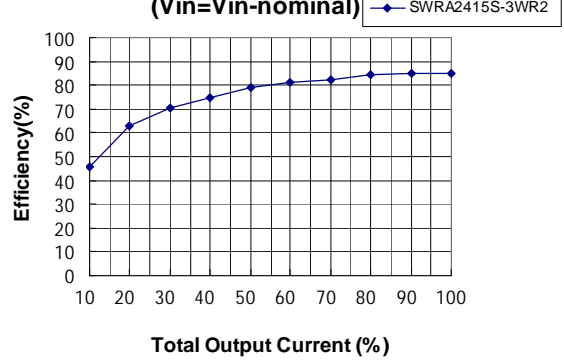


(Figure 5)

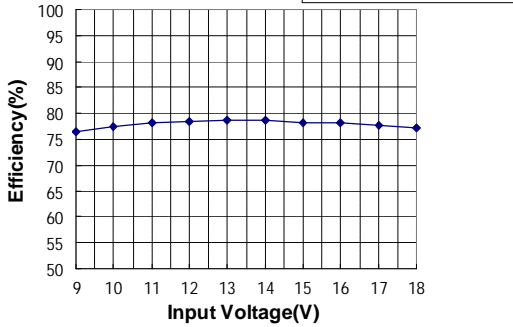
Efficiency VS Input Voltage curve (Full Load)



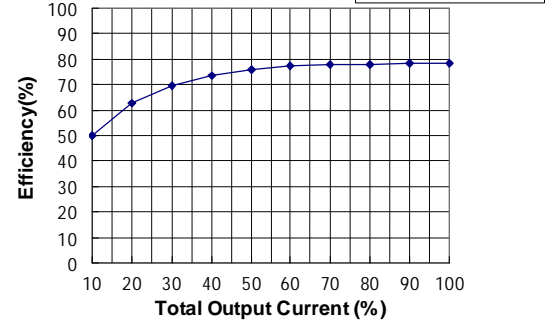
Efficiency VS Output Load curve (Vin=Vin-nominal)



Efficiency VS Input Voltage curve (Full Load)



Efficiency VS Output Load curve (Vin=Vin-nominal)



OUTLINE DIMENSIONS, RECOMMENDED FOOTPRINT & PACKAGING

MECHANICAL DIMENSIONS

PIN CONNECTION

Pin	Single	Dual
1	GND	GND
2	Vin	Vin
3	Ctrl	Ctrl
5	NC	NC
6	+Vo	+Vo
7	0V	0V
8	CS	-Vo

NC: No connection

Note :
Unit:mm[inch]
Pin section tolerances:±0.10[±0.004]
General tolerances:±0.25[±0.01]

THIRD ANGLE PROJECTION

RECOMMENDED FOOTPRINT DETAILS

Note : grid : 2.54*2.54mm

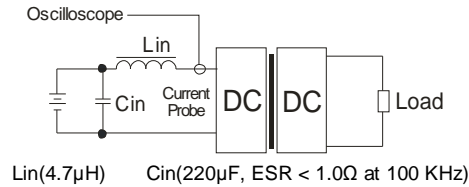
TUBE PACKAGING DIMENSIONS

Note :
Unit:mm[inch]
General tolerances:±0.5[±0.02]
L=530[20.866] Tube Quantity:22 pcs
L=220[8.661] Tube Quantity:8 pcs
Inner carton(S): L*W*H=255*170*80
Outer carton(S): L*W*H=375*280*270, 6 inner cartons(S)
Inner carton(L): L*W*H=580*200*100
Outer carton(L): L*W*H=600*215*220, 2 inner cartons(L)
Outer carton(L): L*W*H=600*215*325, 3 inner cartons(L)

TEST CONFIGURATIONS

Input Reflected-Ripple Current Test Setup

Input reflected-ripple current is measured with an inductor L_{in} and Capacitor C_{in} to simulate the source impedance.



DESIGN CONSIDERATIONS

1) Requirement on output load

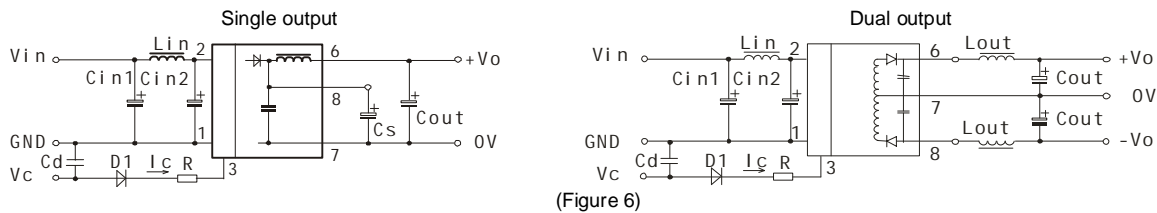
To ensure this module can operate efficiently and reliably, during operation, the minimum output load could not be less than 5% of the full load, otherwise output ripple may increase dramatically. If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load, suppose to use the resistance of 5% rated power, or use our company's products with a lower rated output power.

2) Recommended circuit

All the SWRA_S-3WR2 & SWRB_S-3WR2 series have been tested according to the following recommended test circuit before leaving the factory (see Figure 6).

If you want to further decrease the input/output ripple, you can increase a capacitance-values properly or choose capacitors with low ESR. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor must be less than the Max. Capacitive Load.

General:	Cin1:	5V&12V	100μF
		24V&48V	10μF
	Cin2:	5V&12V	47μF
		24V&48V	1μF
	Lin:	4.7μH~12μH	
	Cs:	10μF~22μF	
	Cout:	100μF(Typ.)	
	Lout:	2.2μH~10μH	
	Cd:	47nF/100V	



(Figure 6)

3) Ctrl Terminal

When open or high impedance, the converter works well; When this pin is 'high', the converter shut down. It should be note that the input current should be between 5-10mA, exceeding the maximum 20mA will cause permanent damage to the converter. The value of R can be derived as follows:

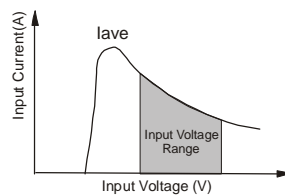
$$R = \frac{V_C - V_D - 1.0}{I_C} - 300$$

For Detailed parameter, please refer to "EMC RECOMMENDED CIRCUIT".

4) Input current

When it is used in unregulated power supply, be sure that the fluctuating range of the power supply and the rippled voltage do not exceed the module standard. Input current of power supply should afford the flash startup average current of this kind of DC/DC module (Figure 7).

General:	Vin:5V	Iave =1110mA
	Vin:12V	Iave =640mA
	Vin:24V	Iave =325mA
	Vin:48V	Iave =160mA



(Figure 7)

5) It is not recommended to increase the output power capability by connecting two or more converters in parallel. The product is not hot-swappable

Note:

1. Min. load shouldn't be less than 5%, otherwise ripple maybe increased dramatically. If the product operates under min. load, it may not be guaranteed to meet all specifications listed. Operation under minimum load will not damage the converter.
2. Recommended Dual output models unbalanced load is $\leq \pm 5\%$, if the product operates $> \pm 5\%$, it may not be guaranteed to meet all specifications listed. Please contact our technical support for more details.
3. Max. Capacitive Load is tested at input voltage range and full load.
4. All specifications measured at $T_a = 25^\circ\text{C}$, humidity $< 75\%$, nominal input voltage and rated output load unless otherwise specified.
5. In this datasheet, all test methods are based on our corporate standards.
6. All characteristics are for listed models, and non-standard models may perform differently. Please contact our technical support for more details.
7. Please contact our technical support for any specific requirement.
8. Specifications of this product are subject to changes without prior notice.