DC/DC Converter SURA_LD-20WR3 & SURB_LD-20WR3 Series



20W,Ultra wide input isolated & regulated dual / single output DC/DC converter



FEATURES

- Wide range of input voltage (4:1)
- Efficiency up to 90%
- No-load power consumption as low as 0.2W
- Isolation voltage: 1500VDC
- Operating temperature range: -40℃ to +85℃
- Input under-voltage protection, output short circuit protection, over-voltage protection, Over-current protection
- Meet CISPR22/EN55022 CLASS A
- Six-sided metal shielding package
- A2S (wring mounting) and A4S (35mm rail mounting) products featuring reverse polarity protection for input

SURA_LD-20WR3 & SURB_LD-20WR3 series are applied to wide voltage range input situation such as data transmission device, battery power supply device, tele-comunication device, distributed power supply system, remote control system, industrial robot system etc.

Selection Guide						
	Input Volta	age (VDC)	Ou	tput	Efficiency ³	Max. Capacitive
Part No. ^①	Nominal (Range)	Max. [®]	Output Voltage (VDC)	Output Current (mA)(Max./Min.)	(%,Min./Typ.) @ Full Load	Load [®] (µF)
SURA2405LD-20WR3			±5	±2000/±100	84/86	4800
SURA2409LD-20WR3			±9	±1111/±56	86/88	1000
SURA2412LD-20WR3			±12	±834/±42	86/88	800
SURA2415LD-20WR3			±15	±667/±33	86/88	625
SURB2403LD-20WR3	24	40	3.3	5000/250	84/86	10000
SURB2405LD-20WR3	(9-36)	40	5	4000/200	88/90	10000
SURB2409LD-20WR3			9	2222/111	87/89	4700
SURB2412LD-20WR3			12	1667/84	87/89	1600
SURB2415LD-20WR3			15	1333/67	88/90	1000
SURB2424LD-20WR3			24	834/42	88/90	500
SURA4805LD-20WR3			±5	±2000/±100	84/86	4800
SURA4812LD-20WR3			±12	±834/±42	86/88	800
SURA4815LD-20WR3			±15	±667/±33	87/89	625
SURB4803LD-20WR3	40		3.3	5000/250	84/86	10000
SURB4805LD-20WR3	48 (18-75)	80	5	4000/200	88/90	10000
SURB4809LD-20WR3	(10 70)		9	2222/111	87/89	4700
SURB4812LD-20WR3			12	1667/84	87/89	1600
SURB4815LD-20WR3			15	1333/67	88/90	1000
SURB4824LD-20WR3			24	834/42	88/90	500

Notes: ①Series with suffix "A45" are heat sink mounting; series with suffix "A25" are chassis mounting, with suffix "A45" are DIN-Rail mounting, for example SURB2405LD-20WHR3A2S is chassis mounting of with heat sink, SURB2405LD-20WR3A4S is DIN-Rail mounting of without heat sink; If the application has a higher requirement for heat dissipation, you can choose modules with heat sink;

②Absolute maximum rating without damage on the converter, but it isn't recommended;

® Efficiency is measured In nominal input voltage and rated output load; A2S (wiring) and A4S (rail) Model due to input reverse polarity protection, minimum efficiency greater than Min.-2 is qualified;

4) The capacitive loads of positive and negative outputs are identical.

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Item	Operating Con-	ditions	Min.	Тур.	Max.	Unit
	04V/DC input	3.3V, 5V output	_	926/40		
langet Commant (full load / no load)	24VDC input	Others	_	926/5		_
Input Current (full load / no-load)	48VDC input	3.3V, 5V output	_	463/20		mA
	46VDC Input	Others	-	463/4		IIIA
Poffortad Dipple Current	24VDC input		_	30		
Reflected Ripple Current	48VDC input		_	30		
Input impulse Voltage (Isse may)	24VDC input		-0.7		50	
Input impulse Voltage (1sec. max.)	48VDC input		-0.7		100	-
Starting Voltage	24VDC input				9	VDC
Sidning vollage	48VDC input				18	VDC
Lindor Voltago Chutdova	24VDC input		5.5	6.5	_	-
Under Voltage Shutdown	48VDC input		14.0	15.5	_	
Starting Time	Nominal input& o	constant resistance load		10	_	ms
Input Filter				Pi fi	ilter	
	Module switch or	١	Ctrl suspende	ed or connected	to TTL high lev	el (3.5-12VDC
Ctrl*	Module switch of	f	Ctrl pin c	onnected to GN	ID or low level	(0-1.2VDC)
	Input current who	en switched off	_	4	7	Ма

Item	Operating Conditions		Min.	Тур.	Max.	Unit
Positive Output Voltage Accuracy				. 1	. 0	
Negative Output Voltage Accuracy				±1	±3	
Balance of Output Voltage	Dual output, balanced l	oad	_	±0.5	±1.5	
Line Regulation	Full load, the input volto to high voltage	age is from low voltage	_	±0.2	±0.5	%
Load Regulation	10%-100% load			±0.5	±1	
Cross Regulation	Dual output, main outpu Supplement output from		_	_	±5	
Transient Recovery Time				300	500	μs
Transient Despense Deviation	25% load step change	3.3V, 5V, ±5V output		±5	±8	%
Transient Response Deviation		Others		±3	±5	76
Temperature Drift Coefficient	Full load		-	±0.02		%/℃
Ripple & Noise	20MHz bandwidth		-	50	100	Mv p-p
Trim				±10		60.4
Output Over-voltage Protection			110		160	%Vo
Output Over-current Protection	Input voltage range		110	_	190	%lo
Output Short circuit Protection			Hic	cup, Continu	ous, self-reco	very

General Specification	ns				
Item	Operating Conditions	Min.	Тур.	Max.	Unit
Insulation Voltage	Input-output, with the test time of 1 minute and the leak current lower than 1mA	1500	_	_	VDC
Insulation Resistance	Input-output, insulation voltage 500VDC	1000			MΩ
Isolation Capacitance	Input-output, 100KHz/0.1V		1050		pF
Operating Temperature	see Fig. 1	-40	_	85	°C
Storage Temperature		-55	_	125	
Storage Humidity	Non-condensing	5		95	%RH
Max. Operating Temperature for casing	Within the operating temperature curve		_	105	°C

DC/DC Converter

SURA_LD-20WR3 & SURB_LD-20WR3 Series

Pin Welding Resistance Temperature	Welding spot is 1.5mm away from the casing, 10 seconds			300	°C
Vibration		10-5	5Hz, 10G, 30 N	1in. along X, Y	and Z
Switching Frequency *	PWM mode	-	270		KHz
MTBF	MIL-HDBK-217F@25℃	1000	_		K hours
Note:* This series of products using	reduced frequency technology, the switching frequency is test y	ratue of full load	When the load	lis reduced to b	pelow 50% the

Note:* This series of products using reduced frequency technology, the switching frequency is test value of full load, When the load is reduced to below 50%, the switching frequency decreases with decreasing load.

Physical Specifi	ications		
Casing Material			Aluminum alloy
	Horizontal package	e(without heat sink)	50.80*25.40*11.80 mm
	Horizontal package	e(with heat sink)	50.80*25.40*16.30 mm
Davidson Discondens	A2S wiring packag	e (without heat sink)	76.00*31.50*21.20 mm
Package Dimensions	A2S wiring packag	e(with heat sink)	76.00*31.50*25.10 mm
	A4S rail package(without heat sink)	76.00*31.50*25.80 mm
	A4S rail package(with heat sink)	76.00*31.50*29.70 mm
\A/-!	without heat sink	Horizontal package/A2S wiring package/A4S rail package	26.00g/48.00g/68.00g(Typ.)
Weight	with heat sink	Horizontal package/A2S wiring package/A4S rail package	34.00g/56.00g/76.00g(Typ.)
Cooling Method			Free air convection

EMC	Specifications			
EN AL	Conducted disturbance		CLASS A (Bare component)/ -② for recommended circuit)	
EMI	Radiated emission		CLASS A (Bare component)/ -② for recommended circuit)	
	Electrostatic discharge	IEC/EN61000-4-2	Contact ±4KV	perf. Criteria B
	Radiation immunity	IEC/EN61000-4-3	10V/m	perf. Criteria A
	EFT	IEC/EN61000-4-4	±2KV (see Fig.3-1) for recommended circuit)	perf. Criteria B
EMS	Surge immunity	IEC/EN61000-4-5	±2KV (see Fig.3-①for recommended circuit)	perf. Criteria B
	Conducted disturbance immunity	IEC/EN61000-4-6	3 Vr.m.s	perf. Criteria A
	Immunities of voltage dip, drop and short interruption	IEC/EN61000-4-29	0-70%	perf. Criteria B

Product Characteristic Curve

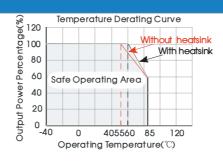
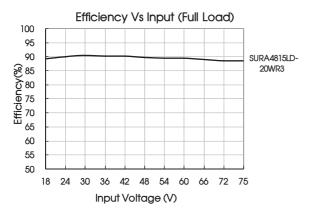
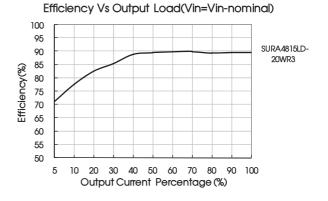
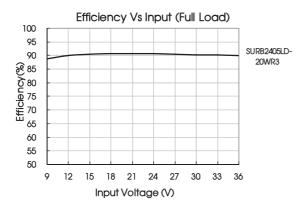


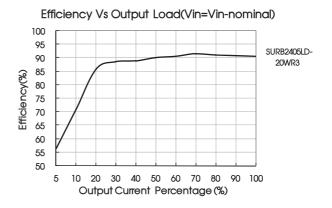
Fig. 1





SURA_LD-20WR3 & SURB_LD-20WR3 Series



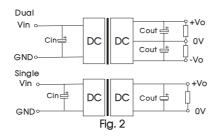


Design Reference

1. Typical application

All the DC/DC converters of this series are tested according to the recommended circuit (see Fig. 2) before delivery.

If it is required to further reduce input and output ripple, properly increase the input & output of additional capacitors Cin and Cout or select capacitors of low equivalent impedance provided that the capacitance is no larger than the max. capacitive load of the product.



Single Vout (VDC)	Cout (µF)	Cin (µF)	Dual Vout (VDC)	Cout (µF)	Cin (µF)
3.3/5	470		±5	220	
9/12/15	220	100	±9/±12/±15	100	100
24	100		_		

2. EMC solution-recommended circuit

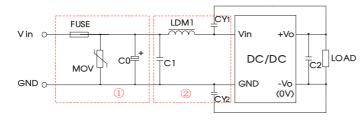


Fig. 3

Notes: Part \odot in the Fig. 3 is used for EMS test and part \odot for EMI filtering; selected based on needs.

Parameter description

Model	Vin:24V	Vin:48V
FUSE	Choose according to	actual input current
MOV	S14K35	S14K30
C0	330µF/50V	330µF/100V
C1	1µF/50V	1µF/100V
C2	Refer to the C	Cout in Fig.2
LDM1	4.7 _L	ıH
CY1, CY2	1nF/2	2KV

EMC solution-recommended circuit PCB layout

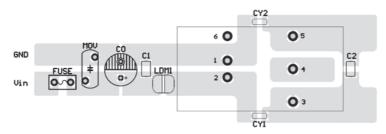
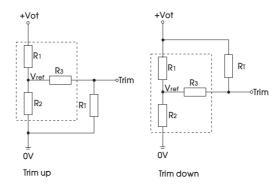


Fig. 4

Note: the min. distance of the bonding pads between input & output isolation capacitors (CY1/CY2) shall be ≥ 2mm.

3. Application of Trim and calculation of Trim resistance



Applied circuits of Trim (Part in broken line is the interior of models)

Calculation formula of Trim resistance:

up:
$$RT = \frac{aR_2}{R_2 - a} - R_3$$
 $a = \frac{Vref}{Vo' - Vref} \cdot R_1$

own: $RT = \frac{aR_1}{R_1 - a} - R_3$ $a = \frac{Vo' - Vref}{Vref} \cdot R_2$

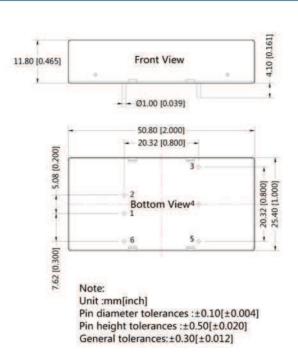
R_T is Trim resistance a is a self-defined parameter, with no real meaning.

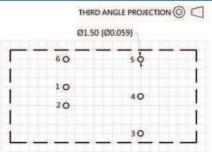
down	D-	aRı	Do	_
down:	KT=	R1-a	-R3	a=

Vout(V)	R1(KΩ)	R2(K Ω)	R3(K Ω)	Vref(V)
3.3	4.801	2.87	12.4	1.25
5	2.883	2.87	10	2.5
9	7.500	2.87	15	2.5
12	11.000	2.87	15	2.5
15	14.494	2.87	15	2.5
24	24.872	2.87	17.8	2.5

4. The product does not support output in parallel with power per liter or hot-plug use

Horizontal Package (without heat sink) Dimensions and Recommended Layout

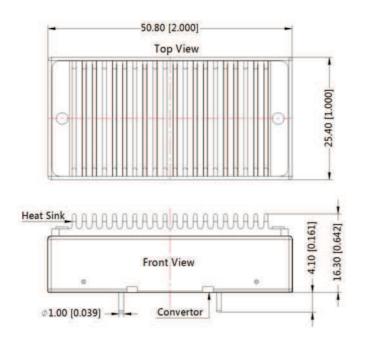




Note: Grid 2.54*2.54mm

	Pin-Out	
Pin	Single	Dual
1	GND	GND
2	Vin	Vin
3	+Vo	+Vo
4	Trim	OV
5	0V	-Vo
6	Ctrl	Ctrl

Horizontal Package (with heat sink) Dimensions



	Pin-Out	
Pin	Single	Dual
1	GND	GND
2	Vin	Vin
3	+Vo	+Vo
4	Trim	OV
5	OV	-Vo

Ctrl

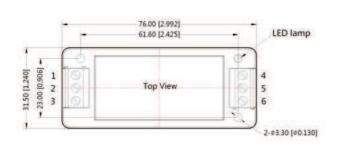
THIRD ANGLE PROJECTION (1)

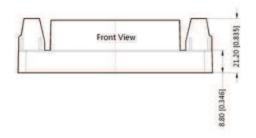
Note: Unit:mm[inch] General tolerances: $\pm 0.50[\pm 0.020]$ If use heatsinks,make sure there is enough space for a special size in ther above graph

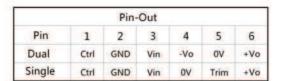
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SURA_LD-20WR3A2S & SURB_LD-20WR3A2S(without heat sink) Dimensions





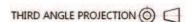


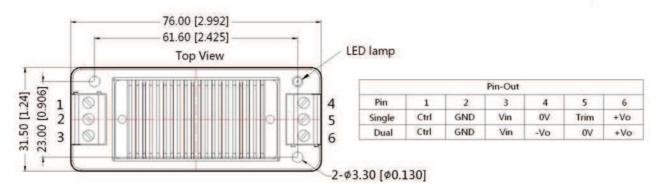
THIRD ANGLE PROJECTION

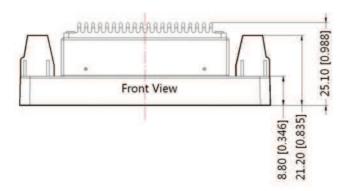
Note: Unit:mm[inch]

Wire range: 24~12 AWG General tolerances:±0.50[±0.020]

SURA_LD-20WHR3A2S & SURB_LD-20WHR3A2S(with heat sink) Dimensions





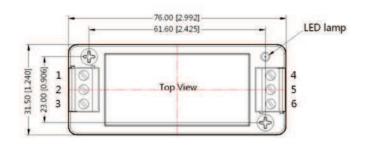


Note: Unit:mm[inch] Wire range:24~12 AWG

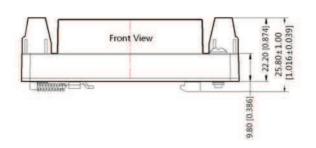
General tolerances: ±0.50[±0.020]

SURA_LD-20WR3A4S & SURB_LD-20WR3A4S(without heat sink) Dimensions





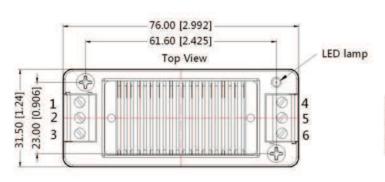
		Pin-	Out			
Pin	1	2	3	4	5	6
Dual	Ctrl	GND	Vin	-Vo	OV	+Vo
Single	Ctrl	GND	Vin	OV	Trim	+Vo



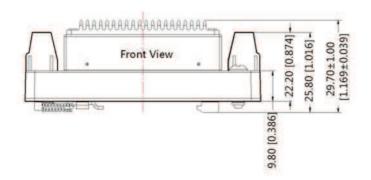
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SURA_LD-20WHR3A4S & SURB_LD-20WHR3A4S(with heat sink) Dimensions





			Pin-Out			
Pin	1	2	3	4	5	6
Single	Ctrl	GND	Vin	ov	Trim	+Vo
Dual	Ctrl	GND	Vin	-Vo	OV	+Vo



Note: Unit:mm[inch] Wire range:24~12 AWG General tolerances:±0.50[±0.020]

Notes:

- Packing Information please refer to 'Product Packing Information'. The Packing bag number of Horizontal package:58200024(without heat sink), 58200043(with heat sink, the Packing bag number of A2S/ A4S package:58220022;
- 2. Recommended used in more than 5% load, if the load is lower than 5%, then the ripple index of the product may exceed the specification, but does not affect the reliability of the product;
- 3. The unbalance degree of the recommended dual output module load: ≤ 5%; if the degree exceeds ±5%, then the product performances cannot be guaranteed to comply with all the performance indicators in the manual, and please directly contact our technicians for specific information;
- The max. capacitive load should be tested within the input voltage range and under full load conditions;
- 5. Unless otherwise specified, data in this datasheet should be tested under the conditions of Ta=25°C, humidity<75% when inputting nominal voltage and outputting rated load;
- 6. All index testing methods in this datasheet are based on our Company's corporate standards;
- The performance indexes of the product models listed in this datasheet are as above, but some indexes of non-standard model
 products will exceed the above-mentioned requirements, and please directly contact our technicians for specific information;
- 8. We can provide product customization service;
- Specifications of this product are subject to changes without prior notice.

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