

SPWE D-6W & SPWF D-6W Series

6W, 4:1 WIDE INPUT, ISOLATED & REGULATED SINGLE/DUAL OUTPUT DIP DC/DC CONVERTER



FEATURES

High Efficiency up to 84%

Operating Temperature: -40°C to +85°C

3KVDC Input/Output Isolation

Short Circuit Protection(Automatic recovery)

Internal SMD construction No Heat Sink Required Industry Standard Pinout MTBF>1,000,000 hours RoHS Compliance

APPLICATIONS

The SPWE_D-6W & SPWF_D-6W Series are specially designed for applications where a wide range input voltage power supplies are isolated from the input power supply in a distributed power supply system on a circuit board.

These products apply to:

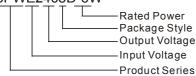
- 1) Where the voltage of the input power supply is wide range (voltage range≤ 4:1);
- 2) Where isolation is necessary between input and output (Isolation Voltage≤3000VDC);
- 3) Where the regulation of the output voltage and the output ripple noise are demanded.

	Input			Output			
Part Number	Voltage (VDC)		Voltage	Current (mA)		Efficiency (%, Typ)	
	Nominal	Range	Max*	(VDC)	Max	Min	(/o, 1 y p)
SPWE2405D-6W	24	9-36	40	±5	±600	±60	80
SPWE 2412D-6W				±12	±250	±25	82
SPWE 2415D-6W				±15	±200	±20	84
SPWF2403D-6W				3.3	1500	150	78
SPWF2405D-6W				5	1200	120	80
SPWF2412D-6W				12	500	50	82
SPWF2415D-6W				15	400	40	84
SPWF2424D-6W				24	250	25	82
SPWE4805D-6W	48	18-72	80	±5	±600	±60	80
SPWE4812D-6W				±12	±250	±25	82
SPWE4815D-6W				±15	±200	±20	84
SPWE4824D-6W				±24	±125	±13	84
SPWF4803D-6W				3.3	1500	150	77
SPWF4805D-6W				5	1200	120	80
SPWF4812D-6W				12	500	50	82
SPWF4815D-6W				15	400	40	84
SPWF4824D-6W				24	250	25	84

Note:

- 1.*Input voltage can't exceed this value, or will cause the permanent damage.
- 2. The load shouldn't be less than 10%, otherwise ripple will increase dramatically.
- Operation under 10% load will not damage the converter; However, they may not meet all specification listed.

MODEL SELECTION	
SPWE2405D-6W	
TTTT .	



ducts program	Min 0.6	Тур	Max 6	Units	
	0.6				
mended circuit			J	W	
		±1	±3		
mended circuit		±3	±5	%	
100% load ±0.5		±2*	70		
rom low to high	to high ±0.2 ±		±0.5]	
mended circuit		±0.02		%/°C	
idth		20	50	mVp-p	
idth		75	150		
minal Input voltage		300		KHz	
i	imended circuit	imended circuit idth	±0.02 ±0.02	imended circuit ±0.02 idth 20 50 idth 75 150	

^{*}Dual output models unbalanced load: ±5%

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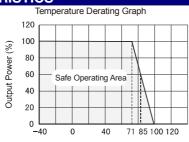
^{**}Test ripple and noise by "parallel cable" method. See detailed operation instructions at Testing of Power Converter section, application notes.

COMMON SPECIFICA	ATION				
Item	Test Conditions	Min	Тур	Max	Units
Storage Humidity				95	%
Operating Temperature		-40		85	
Storage Temperature		-55		125] _
Temp. rise at full load			40		
Lead Temperature	1.5mm from case for 10 seconds			300	
Isolation voltage	Tested for 1 minute and 1mA max	3000			VDC
Isolation resistance	Test at 500VDC	1000			МΩ
No-load power consumption			500		mW
Cooling		Free air convection			n
Case Material		Plastic(UL94-V0)			
Short Circuit Protection		Continuous, automatic recover			ecovery
MTBF		1000			K hours
Weight			17		g

Note:

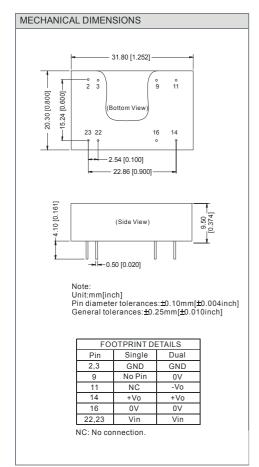
- All specifications measured at T_A=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.
- 2. See below recommended circuits for more details.

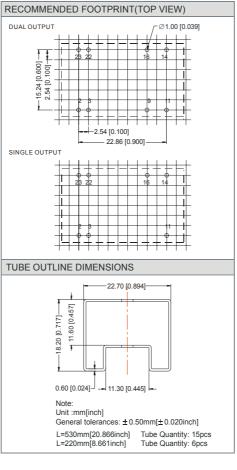
TYPICAL CHARECTERISTICS



Operating Temp.(°C)

OUTLINE DIMENSIONS & FOOTPRINT DETAILS





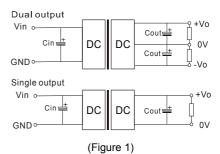
APPLICATION NOTE

Requirement Output Load

In order to ensure the product operate efficiently and reliably, in addition to a max load (namely full load), a minimum load is specified for this kind of DC/DC converter. Make sure the specified range of input voltage is not exceeded, the minimum output load no less than 10% load. If the actual load is less than the specified minimum load, the output ripple may increase sharply while its efficiency and reliability will reduce greatly. If the actual output power is very small, please add an appropriate resistor as extra loading, or contact our company for other lower output power products.

Recommended Circuit

All the SPWE_D-6W & SPWF_D-6W Series have been tested according to the following recommended testing circuit before leaving factory. (See Figure 1).



If you want to further decrease the input/output ripple, you can increase capacitance properly or choose capacitors with low ESR. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor sees (Table 1). General:

Cin: 24V&48V 10µF-47µF

Cout: 10µF/100mA

Output External Capacitor Table (Table 1)

Single	Cout	Daul Vout	Cout
Vout	(uF)	(VDC)	(uF)
(VDC)			
3.3	2200	±5	680
5	1000	±12	330
12	470	±15	220
15	330	±24	100
24	220	-	-

Input Current

When it is used in unregulated power supply, be sure that the fluctuating range of the power supply and the rippled voltage do not exceed the module standard. Input current of power supply should afford the startup current of this kind of DC/DC module (See figure 2), General:

Ip ≤1.6*lin-max



No parallel connection or plug and play.