Rol

SF_RN-1W & SF_RT-1W Series

1W, FIXED INPUT, ISOLATED & UNREGULATED SINGLE OUTPUT, DC-DC CONVERTER



1 Year Warranty

SCHMID-



Small Footprint, Very thin package 3KVDC Isolation Temperature Range: -40°C to +85°C No Heat sink Required High Power Density No External Component Required Industry Standard Pinout Pin-out compatible with DCP01 Series Short circuit protection RoHS Compliance

APPLICATIONS

The SF_RN-1W & SF_RT-1W Series are specially designed for applications where a group of polar power supplies are isolated from the input power supply in a distributed power supply system on a circuit board. These products apply to:

1) Where the voltage of the input power supply is fixed (voltage variation $\leq \pm 10\%$);

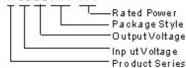
 Where isolation is necessary between input and output (isolation voltage ≤3000VDC);

3) Where the regulation of the output voltage and the output ripple noise are not demanding.

Such as: purely digital circuits, ordinary low frequency analog circuits, and IGBT power device driving circuits.

MODEL SELECTION





PRODUCT PROGRAM									
Part Number	Input		Output						
	Voltage (VDC)		Voltage	Current (mA)		Efficiency (%, Typ.)	Package		
	Nominal	Range	(VDČ)	Max	Min	(,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,			
SF0505RN-1W	5	4.5-5.5	5	200	200	5 200 20 74	20	74	DIP
SF0505RT-1W	5	4.5-5.5	5	200	20	/4	SMD		

Item	Test Conditions	Min	Тур	Max	Units	
Storage humidity				95	%	
Operating temperature		-40		85		
Storage temperature		-55		125	°C	
Temp. rise at full load			15	25	C	
Lead temperature	1.5mm from case for 10 seconds			260		
Short circuit protection			Continuous			
Cooling			Free air convection			
Package material		Epo	Epoxy Resin (UL94-V0)			
MTBF		3500			K hours	
Weight			1.4		g	

ISOLATION SPECIFICATIONS						
Item	Test Conditions	Min	Тур.	Max	Units	
Isolation voltage	Tested for 1 minute and 1mA max	3000			VDC	
Isolation resistance	Test at 500VDC	1000			MΩ	

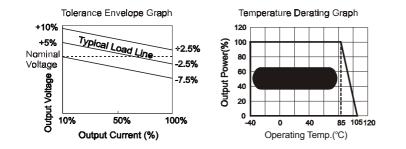
OUTPUT SPECIFICATIONS						
Item	Test Conditions		Тур.	Мах	Units	
Output power				1	W	
Line regulation	For Vin change of 1%			±1.2	%	
Load regulation	10% to 100% load (5V output)		12.8	15	70	
Output voltage	See tolerance envelope graph					
Temperature drift Nominal input,100% full load				0.03	%/°C	
Dianta Oblaiaa*	20MHz Bandwidth, Nominal input,50% load	20		m)/n n		
Ripple &Noise*	20MHz Bandwidth, Nominal input,100% full		50	75	mVp-p	
Switching frequency Full load, nominal input			100		KHz	
*Test ripple and noise by "parallel cable" method. See detailed operation instructions at Testing of Power Converter section, application notes.						

Note:

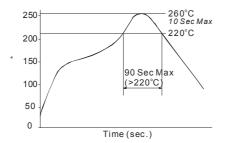
1. All specifications measured at $T_A=25^{\circ}$ C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.

2. See below recommended circuits for more details.

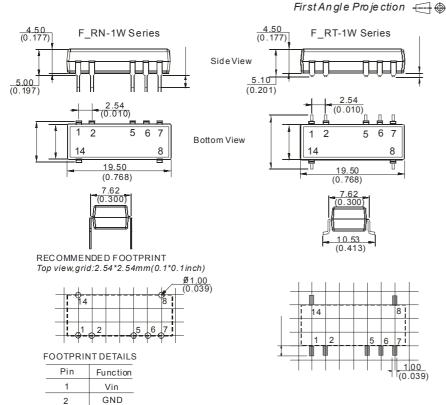
TYPICAL CHARACTERISTICS



RECOMMENDED REFLOW SOLDERING PROFILE



OUTLINE DIMENSIONS & FOOTPRINT DETAILS



0 V

+Vo

NC

Pin section: 0.50*0.25mm(0.020*0.010inch)

General tolerances: ±0.25mm(±0.006inch)

Pin section tolerances: ±0.10mm(±0.004inch)

5

6

7.8.14

Unit:mm(inch)

Note:

NC:No Connection

Pin	Function		
1	Vin		
2	GND		
5	0 V		
6	+Vo		
7,8,14	NC		
NC:No Connection			

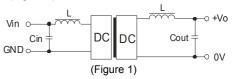
APPLICATION NOTE

Requirement on output load

To ensure this module can operate efficiently and reliably, During operation, the minimum output load is **not less than 10%** of the full load, and that **this product should never be operated under no load!** If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load, or use our company's products with a lower rated output power.

Recommended testing circuit

If you want to further decrease the input/output ripple, an "LC" filtering network may be connected to the input and output ends of the DC/DC converter, see (Figure 1).



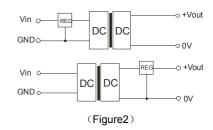
It should also be noted that the inductance and the frequency of the "LC" filtering network should be staggered with the DC/DC frequency to avoid mutual interference. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor sees (Table 1)

	r capacitor sees (lable 1).								
E	EXTERNAL CAPACITOR TABLE (Table 1)								
	Vin	Cin	Vout	Cout					
	(VDC)	(uF)	(VDC)	(uF)					
	5	4.7	5	10					

It's not recommend to connect any external capacitor in the application field with less than 0.5 watt output.

Output Voltage Regulation and Over-voltage Protection Circuit

The simplest device for output voltage regulation, over-voltage and over-current protection is a linear voltage regulator with overheat protection that is connected to the input or output end in series (Figure2).



Overload Protection

Under normal operating conditions, the output circuit of these products has no protection against over load. The simplest method is to connect a self-recovery fuse in series at the input end or add a circuit breaker to the circuit.

No parallel connection or plug and play.